



#### N-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

#### **Features**

- Low On-Resistance
  - 90 mΩ @ V<sub>GS</sub> = 4.5V
  - 110 mΩ @ V<sub>GS</sub> = 2.5V
  - 200 mΩ @ V<sub>GS</sub> = 1.5V
- Very Low Gate Threshold Voltage
- Low Input Capacitance
- ESD Protected Gate
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

# Case: SOT-23

**Mechanical Data** 

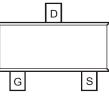
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.008 grams (approximate)



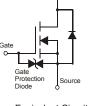
ESD PROTECTED TO 3kV







Equivalent Circuit



Drain



TOP VIEW

#### Ordering Information (Note 4)

Part Number	Case	Packaging
DMN3200U-7	SOT-23	3000/Tape & Reel

SOT-23

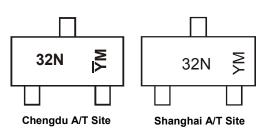
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. Notes:

2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

## Marking Information



32N = Marking Code YM = Date Code Marking for SAT (Shanghai Assembly/ Test site)  $\overline{Y}_{M}$  = Date Code Marking for CAT (Chengdu Assembly/ Test site) Y or  $\overline{Y}$  = Year (ex: A = 2013) M = Month (ex: 9 = September)

#### Date Code Key

Year	2007	2008	2009	2010	201	1 20	012	2013		2014	2015	2016	2017
Code	U	V	W	Х	Y		Z	А		В	С	D	Е
Month	Jan	Feb	Mar	Apr	Мау	Jun	Ju		Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7		8	9	0	N	D



# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Drain-Source Voltage	V <sub>DSS</sub>	30	V
Gate-Source Voltage	V <sub>GSS</sub>	±8	V
Drain Current (Note 5)	ID	2.2	А
Pulsed Drain Current (Note 5)	I <sub>DM</sub>	9	A

#### Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 5)	PD	650	mW
Thermal Resistance, Junction to Ambient	R <sub>θJA</sub>	192	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	30	_	_	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250µA
Zero Gate Voltage Drain Current	IDSS	_	_	1	μA	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	_		±5	μA	$V_{GS} = \pm 8V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 6)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	0.45	_	1.0	V	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$
		_	62 70 150	90 110 200	mΩ	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 2.2A
Static Drain-Source On-Resistance	R <sub>DS (ON)</sub>					V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 2A
						V <sub>GS</sub> = 1.5V, I <sub>D</sub> = 0.67A
Forward Transfer Admittance	Y <sub>fs</sub>	_	5	_	S	V <sub>DS</sub> =5V, I <sub>D</sub> = 2.2A
Diode Forward Voltage (Note 6)	V <sub>SD</sub>	_	_	0.9	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 1A
DYNAMIC CHARACTERISTICS (Note 7)	•				•	
Input Capacitance	Ciss		290		pF	
Output Capacitance	Coss	_	66	_	pF	V <sub>DS</sub> = 10V, V <sub>GS</sub> = 0V f = 1.0MHz
Reverse Transfer Capacitance	C <sub>rss</sub>		35		pF	

5. Device mounted on FR-4 PCB, on minimum recommended pad layout on 2oz. Copper pads.

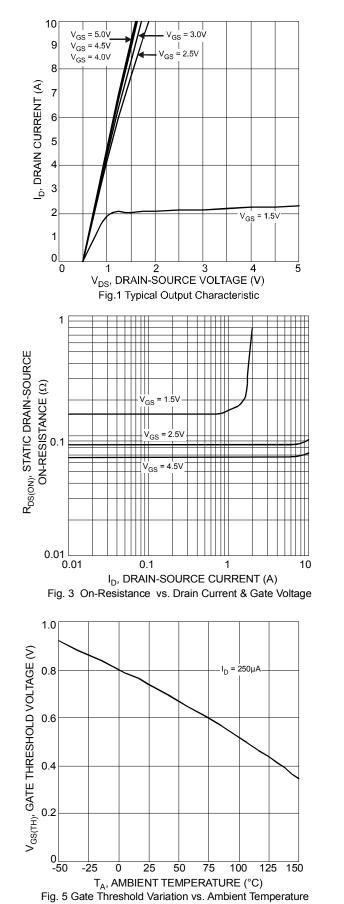
6. Short duration pulse test used to minimize self-heating effect.

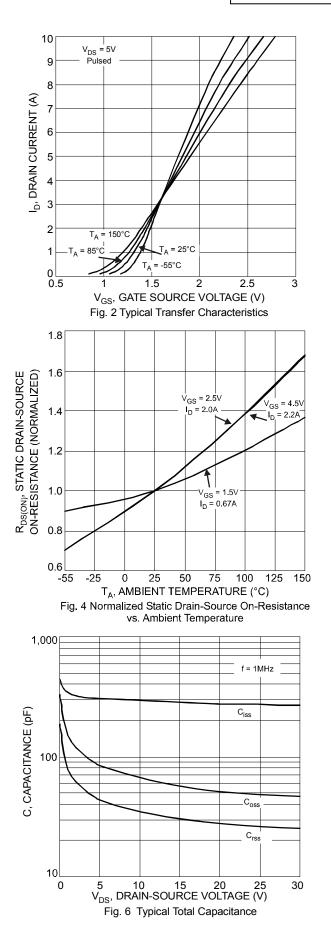
7. Guaranteed by design. Not subject to product testing.

Notes:



#### DMN3200U



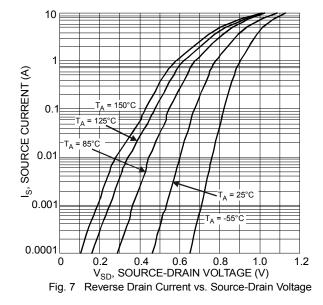


NEW PRODUCT

DMN3200U Document number: DS31188 Rev. 5 - 2

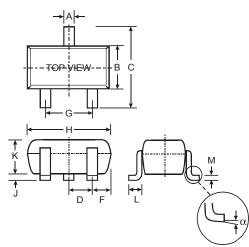


### DMN3200U



## **Package Outline Dimensions**

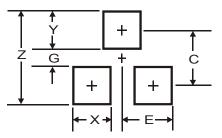
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



	SOT23	_
Dim	Min	Max
Α	0.37	0.51
В	1.20	1.40
С	2.30	2.50
D	0.89	1.03
F	0.45	0.60
G	1.78	2.05
Н	2.80	3.00
J	0.013	0.10
K	0.903	1.10
L	0.45	0.61
М	0.085	0.180
α	0°	8°
All Dir	nensions	in mm

## **Suggested Pad Layout**

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



Dimensions	Value (in mm)
Z	3.4
G	0.7
Х	0.9
Y	1.4
С	2.0
E	0.9



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